

Appl. No. 09/755,312
Amdt. dated January 11, 2006
Reply to Office Action of July 13, 2005

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

Please cancel claims 2, 3, 7-10, and 16-19 without prejudice.

Please amend claims 1, 4-6, 11-13, and 15 as follows:

1. (currently amended): A method of ~~parallel-programming~~ an electronic device's memory with test code and system code ~~prior to board level testing during manufacturing~~, the method comprising the steps of:

programming said electronic device memory with ~~first instructions and second instructions~~ code common to a plurality of electronic devices, wherein ~~said first instructions~~ a first portion of the common code comprises said test code for use during board level testing of said electronic device, and wherein ~~said second instructions~~ a second portion of the common code comprises partial system code for system level testing of said electronic device;

executing said ~~first instructions~~ test code portion of the common code during board level testing of said electronic device to determine the condition of said electronic device ~~independent of said second instructions~~;

programming said electronic device memory with ~~a third instructions~~ portion of code if the electronic device passes board level testing, wherein said third

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~~instructions~~portion of code includes system code to complement said second

~~instructions~~portion of common code and customize said electronic device; and

executing said second ~~instructions~~ and said third ~~instructions~~portions of code

during system level testing of said electronic device.

claims 2-3 (cancelled)

4. (currently amended): The method of Claim 1, wherein said memory ~~location is~~
 flash memory.

5. (currently amended): The method of Claim 1, wherein said step of programming
 said electronic device with said third ~~instructions~~portion of code comprises overwriting said first
~~instructions~~portion of code with said ~~second~~third instructionsportion of code.

6. (currently amended): The method of Claim 1, wherein said third ~~instructions~~
~~portion of code~~ arecomprises user interface codes.

claims 7-10 (cancelled)

11. (currently amended): A system for ~~parallel~~ programming an electronic device's
 memory with test code and system code ~~prior to board level testing during manufacturing~~, the
 system comprising:

means for programming said electronic device memory with ~~first instructions and~~
~~second instructions~~code common to a plurality of electronic devices, wherein said ~~first~~
~~instructions~~a first portion of the common code comprises said test code for use during
 board level testing of said electronic device, and wherein said ~~a~~ second ~~instructions~~portion

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of the common code comprises partial system code for system level testing of said electronic device;

means for executing said ~~first instructions~~ test code portion of the common code during board level testing of said electronic device to determine the condition of said electronic device ~~independent of said second instructions~~;

means for programming said electronic device with a ~~third instructions~~ portion of code, wherein said third ~~instructions~~ portion of code includes system code which complements said second ~~instructions~~ portion of code to complete said system code and customize said electronic device, if the electronic device passes board level testing; and

means for executing said second ~~instructions~~ and said third ~~instructions~~ portions of code during system level testing of said electronic device.

12. (currently amended): A system for ~~parallel-programming~~ an electronic device's memory with test code and system code ~~prior to board level testing during manufacturing~~, the system comprising:

an electronic device;

a programmable memory located in said electronic device, ~~the programmable memory having an input for receiving code, wherein the programmable memory is programmed with code common to a plurality of portable electronic devices.~~

~~a first input device in communication with said electronic device, wherein first instructions and second instructions are programmed into said programmable memory by said input device, and wherein said a first instructions~~ portion of common code comprises

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~~said test code, and said~~ a second instructions portion of common code comprises said
partial system code for system level testing of said electronic device;

means for executing said ~~first instructions~~ test code portion of the common code
stored in said electronic device during board level testing of said electronic device to
determine the condition of said electronic device;

~~a second input device in communication with said electronic device, wherein third~~
~~instructions are programmed into said programmable memory to complement said second~~
~~instructions to complete said system code; and~~

means for programming said programmable memory with a third portion of code,
wherein the third portion of code includes system code to complement said second
portion of common code and customize said electronic device;

means for executing said second ~~instructions~~ and said third ~~instructions~~ portions of
code stored in said electronic device during system level testing of said electronic device.

13. (currently amended): The system of Claim 12; further comprising a wherein said
first and second input devices are the same input device for programming said first instructions,
said second instructions, and said third instructions into said programmable memory.

14. (original): The system of Claim 12, wherein said programmable memory is flash
RAM.

15. (currently amended): The system of Claim 12, wherein said third
instructions portion of code ~~are~~ comprises user interface codes.

claims 16-19 (cancelled)

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20. (original): The system of Claim 12, wherein said programmable memory is flash memory.

Please add the following new claims.

21. (new): The method of Claim 1 further comprising:
programming said memory with consumer preference information to customize the electronic device for an individual consumer, if the electronic device passes system level testing.

22. (new): The method of Claim 21 wherein the consumer preference information customized to an individual consumer includes at least one of a phone number, a speed dial number, or a ring volume setting.

23. (new): The method of Claim 1 wherein during the step of programming said electronic device memory with the third portion of code, said test code portion is overwritten.

24. (new): The method of Claim 23 wherein the third portion of code includes operational mode code.

25. (new): The method of claim 24 wherein the operational mode code defines whether the device has dual, single, or tri-mode phone capability.

26. (new): The method of Claim 1 wherein the electronic device is a cellular phone or a personal digital assistant.

27. (new): The system of Claim 12 further comprising:
a first input device for programming said first instructions and said second instructions into said programmable memory; and

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a second input device for programming said third instructions into said programmable
memory.